LM3402HV Design Document

National Semiconductor LM3402HV October 2006



1.0 Design Specifications

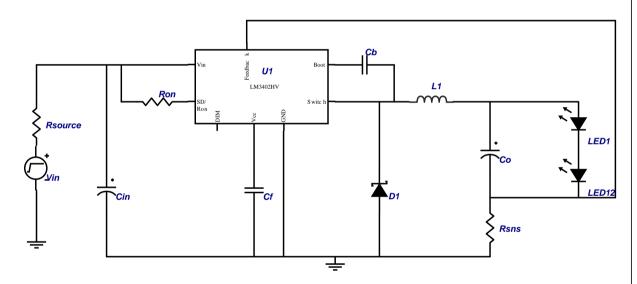
Inputs	Outputs #1
VinMin=8	Vout1=5.2
VinMax=75	lout1=0.350

2.0 Design Description

This circuit has been designed to drive a string of two AlInGaP red or amber LEDs (total Vo = 5.2V) at a forward current of 350 mA +-10% mA with a peak-to-peak ripple current of 50 mA or less. The input is 8V to 75V, with LED current and switching frequency (500 kHz) centered at VIN = 48V.

This design uses a small output capacitor to maintain low ripple current through the LED while allowing higher ripple current in the switch/inductor.

3.0 Schematic



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FIGURE 1. Example Schematic Showing Connection for all Components.

4.0 Bill Of Materials

Part	Manufacturer	Part#	Attributes
Cb	Vishay	VJ0805Y103KXXAT	10n F
Cf	Vishay	VJ0805Y104KXXAT	100n F
Cin	TDK	C5750X7R2A335M	NumCaps=1, 3.3u F
Со	Vishay	VJ1206Y224KXXAT	220n F
D1	Central Semiconductor	CMSH1-100M	0.5 V
L1	TDK	SLF7045T-101MR60-1PF	100u H, 0.39 Ohms
Ron	Vishay	CRCW08057872F	78.7k Ohms
Rsns	Panasonic	ERJ6BQFR62V	0.62 Ohms
U1	National Semiconductor	LM3402HV	

LM3402HV

Notes

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